

Form 1449*	Atty. Docket No.: 884.229US1	Serial No. Unknown
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Rajendran Nair et al.	
	Filing Date: Herewith	Group: Unknown

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09/46/742

12/14/99

U.S. PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
JIC	5,173,835	12/22/1992	Cornett, K.D., et al.	257	310	10/15/91
JR	5,405,790	04/11/1995	Rahim, I., et al.	437	34	11/23/93
JR	5,563,779	10/08/1996	Cave, M.D., et al.	363	59	12/05/94
JR	5,615,096	03/25/1997	Stengel, R.E., et al.	363	60	06/06/94
JR	5,965,912	10/12/1999	Stolfa, D.L., et al.	257	312	09/03/97

FOREIGN PATENT DOCUMENTS

**Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
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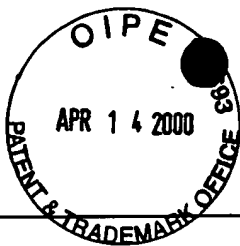
OTHER DOCUMENTS

**Examiner Initial	(Including Author, Title, Date, Pertinent Pages, Etc.)
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Examiner	Jung Kim	Date Considered	7-3-01
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*Substitute Disclosure Statement Form (PTO-1449)

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Sheet 1 of 1

Form 1449*	Atty. Docket No.: 884.229US1	Serial No. 09/460,742
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)	Applicant: Rajendran Nair et al.	
	Filing Date: December 14, 1999	Group: 2815

U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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**Examiner Initial	
JK	Pierret, R.F., et al., "Field Effect Devices", <u>Volume IV of Modular Series on Solid State Devices, 2nd Edition</u> , Addison-Wesley Publishing Company, 47-59, (1990)
JK	Weste, N.H., et al., "Principles of CMOS VLSI Design: A Systems Perspective", <u>2nd Edition</u> , Addison-Wesley Publishing Company, 41-51 and 180-187, (1993)

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Examiner	Jung Kim	Date Considered	7-301
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